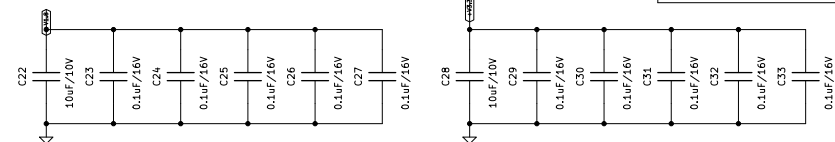
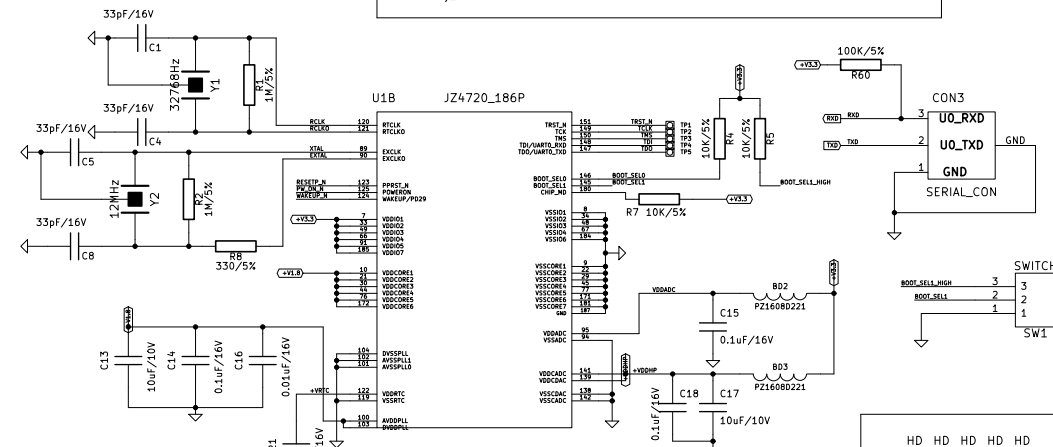
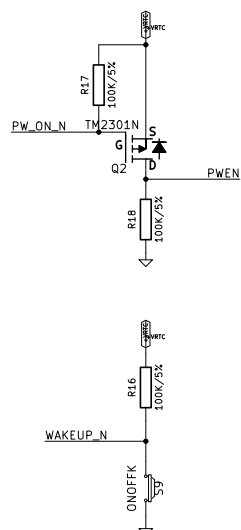
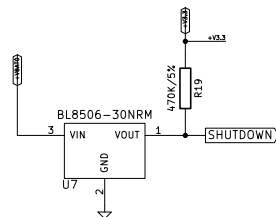
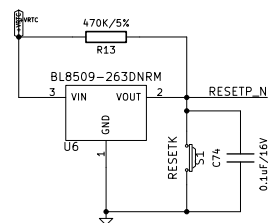
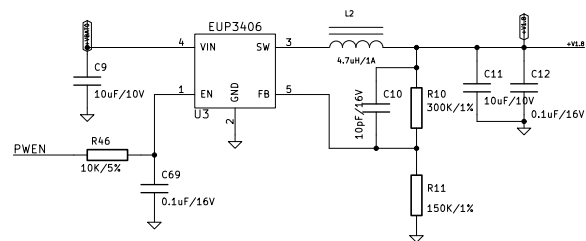
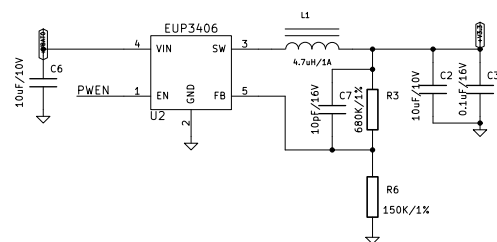
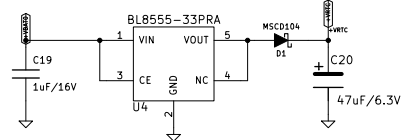


[illegible]

TESTPOINT

TP32 X

TESTPOINT

TP78 X

TESTPOINT

TP77 X

TESTPOINT

TP76 X

TESTPOINT

TP6 X

TESTPOINT

TP7 X

TESTPOINT

TP8 X

TESTPOINT

TP9 X

TESTPOINT

TP10 X

CHARGE_N

SHUTDOWN

WAKEUP_N

SMD

RESET_N

+VBUS

+VBAT

+V3.3

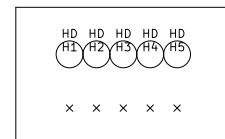
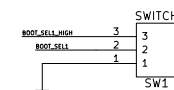
+V1.8

memory.sch

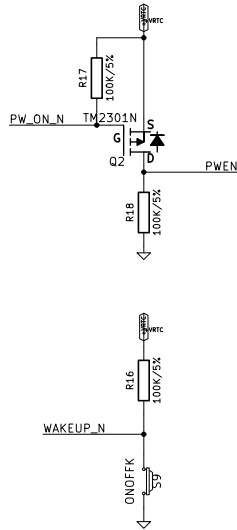
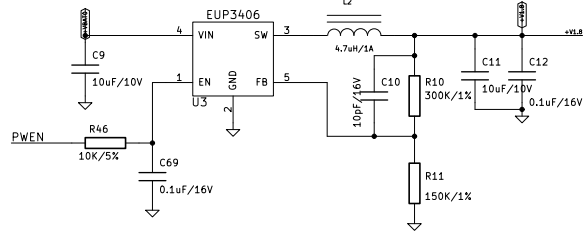
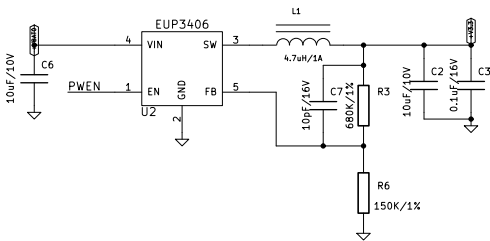
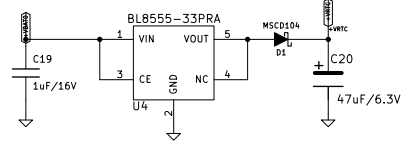
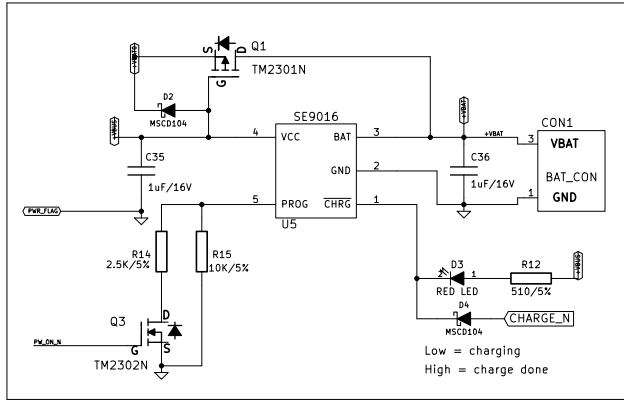
lcd_usb.sch

KEY_MATRIX

power_tree.sch



Charge Battery From USB



Test Points For Power

