



Battery

C4  
10uF

Disable

Boost  
120 mA

C5+C9  
24uF

POR  
4.0V

$V_{max} = V_{BUS}(min) = 4.375\text{ V [USB 2.0]}$   
 $V_{min} = 3.6\text{ V [KL26]}$   
 $(V_{reg33out}(min) = 3\text{ V @ } V_{REGIN} = 3.6\text{ V})$

C36  
0.1uF

D3

Reverse leakage

C16  
1uF

VREG  
(cMCU)  
120 mA

C17  
2.2uF

3V3

220 mA  
*Extreme peak*

sMCU  
20 mA

5 mA

C22+C23  
1.1uF

cMCU  
20 mA

5 mA

C18+C19  
1.1uF

RF  
25 mA

5 mA

VRF

C37  
10uF

Q3

cMCU

rfkill

C25+C26+C32  
0.2uF

C24+C25+C26+C32  
5.8uF

DC-DC configuration shown in green

C29+C33+C34  
0.2uF

Power sequencing avoids extreme peaks (i.e., simultaneous peak demand by multiples subsystems) in normal operation. Components are rated to withstand extreme peaks, but system is likely to reset.

U2

DISP\_VDD

C2  
1uF

sMCU

Power saving

C6+C12+C13  
2.1uF

OLED  
25 mA

VCC/VCOMH

C14+C15  
6.9uF

C10+C11  
2uF

U2

CARD\_VDD

C1  
1uF

sMCU

Power saving

C3  
0.1uF

Memcard  
75 mA

75 mA, 250 ms  
2.7-3.6 V